

**Amendments to the Specification:**

Please replace paragraph [0031], page 12<sup>11</sup> with the following rewritten paragraph:

ycc  
8/23/07

Next, the control unit 70 will be discussed. FIG. 3 is a block diagram to show the configuration of the control unit 70 and the components related to the control unit 70. The control unit 70 includes a CPU 71, ROM 72 storing various control programs, RAM 73 including various types of memory such as a reception buffer for receiving and storing transmission data transmitted from an external data transmission machine PC such as a personal computer or a host computer, a timing control circuit (TC) 74 for generating a timing signal for the write/read timing of reception data into/from the reception buffer, an interface (I/F) 75 for receiving transmitted print data, a video interface (V I/F) 76 having a scan buffer for outputting print information converted into bit image data to a DC controller circuit 82 in order, a sensor interface (S I/F) 77 for receiving detection signals from a toner sensor 44, paper feed ~~sensors~~, sensors 200, such as the original front sensor 37 and the original rear sensor 38, and other sensors, and a panel interface (P I/F) 78 for receiving a switch signal for selection of any of various control modes from the operation panel 14; the components are connected to the CPU 71 by a bus 81.